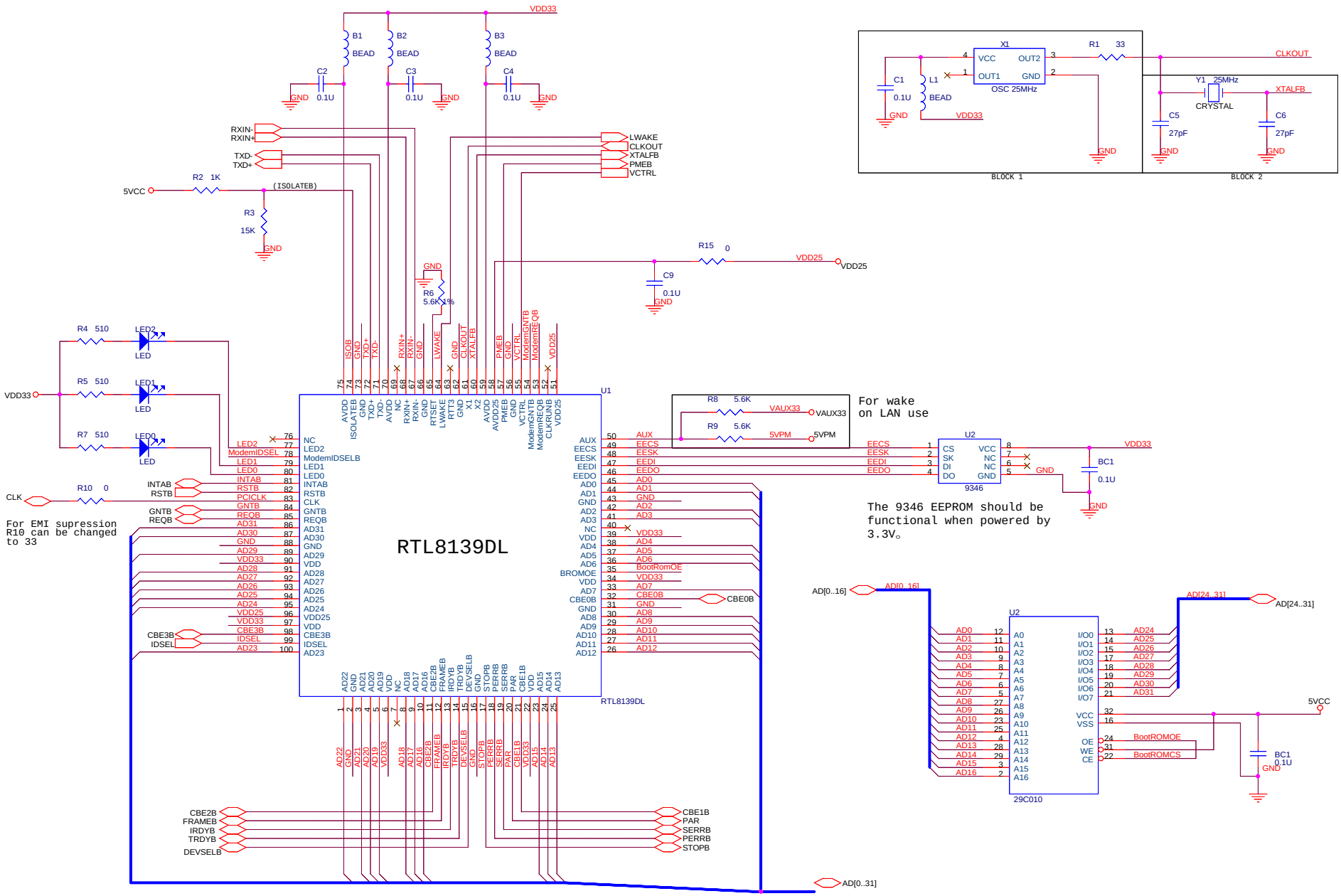

RTL8139DL
U1
VDD33
B1 BEAD
B2 BEAD
B3 BEAD
C2 0.1U
C3 0.1U
C4 0.1U
GND
RXIN-
RXIN+
TXD-
TXD+
LWAKE
CLKOUT
XTALFB
PMEB
VCTRL
5VCC
R2 1K
(ISOLATEB)
R3 15K
R6 5.6K 1%
R15 0
VDD25
C9 0.1U
R4 510
R5 510
R7 510
LED2
LED1
LED0
LED
CLK
R10 0
INTAB
RSTB
GNTB
REQB
CBE3B
IDSEL
CBE2B
FRAMEB
IRDYB
TRDYB
DEVSELB
CBE1B
PAR
SERRB
PERRB
STOPB
AD[0..31]
For EMI supression
R10 can be changed
to 33
R8 5.6K
R9 5.6K
VAUX33
5VPM
For wake
on LAN use
U2
9346
EECS
EESK
EEDI
EEDO
BC1 0.1U
The 9346 EEPROM should be
functional when powered by
3.3V。
AD[0..16]
AD[16..31]
AD[24..31]
29C010
BootROMOE
BootROMCS
X1
OSC 25MHz
R1 33
CLKOUT
Y1 25MHz
CRYSTAL
C5 27pF
C6 27pF
XTALFB
BLOCK 1
BLOCK 2
Realtek RTL8139DL PCI BOARD
Document Number
RED8139DL
Rev
1.10
Size
A3
Date: Friday, November 09, 2001
Sheet 1 of 4