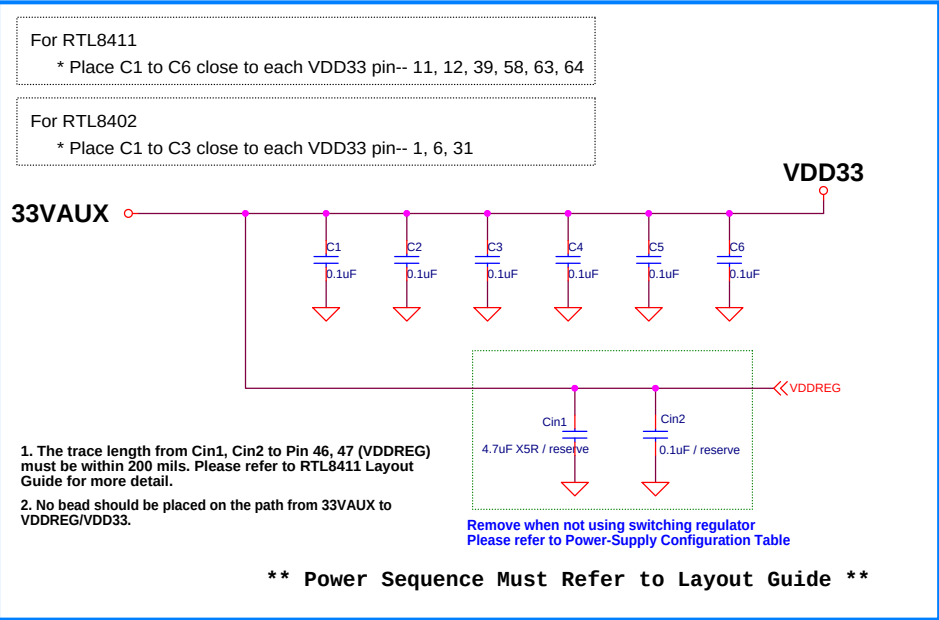


RTL8411/RTL8402 64/48 Pin Reference Schematic V103

PAGE INDEX

- 1. Title Page
- 2. Power
- 3. QFN64 Controller
- 4. QFN48 Colay
- 5. Card Reader
- 6. Optional Function
- 7. Change Notes

3.3V Power



Power-Supply Configuration Table

	mode	Cin1,2	R2	Lx	Cout1,2	R3	R30	R33	C44
RTL8411	SWR	O	X	O	O	O	O	X	X
RTL8411	LDO	X	O	X	X	O	X	O	X
RTL8411	External	X	X	X	X	O	X	O	O
RTL8402	LDO	X	X	X	X	X	X	X	X

**** If only one 1.0V supply source is selected. You may do the following option to save RBOM ****

RTL8411 only (No Colay)

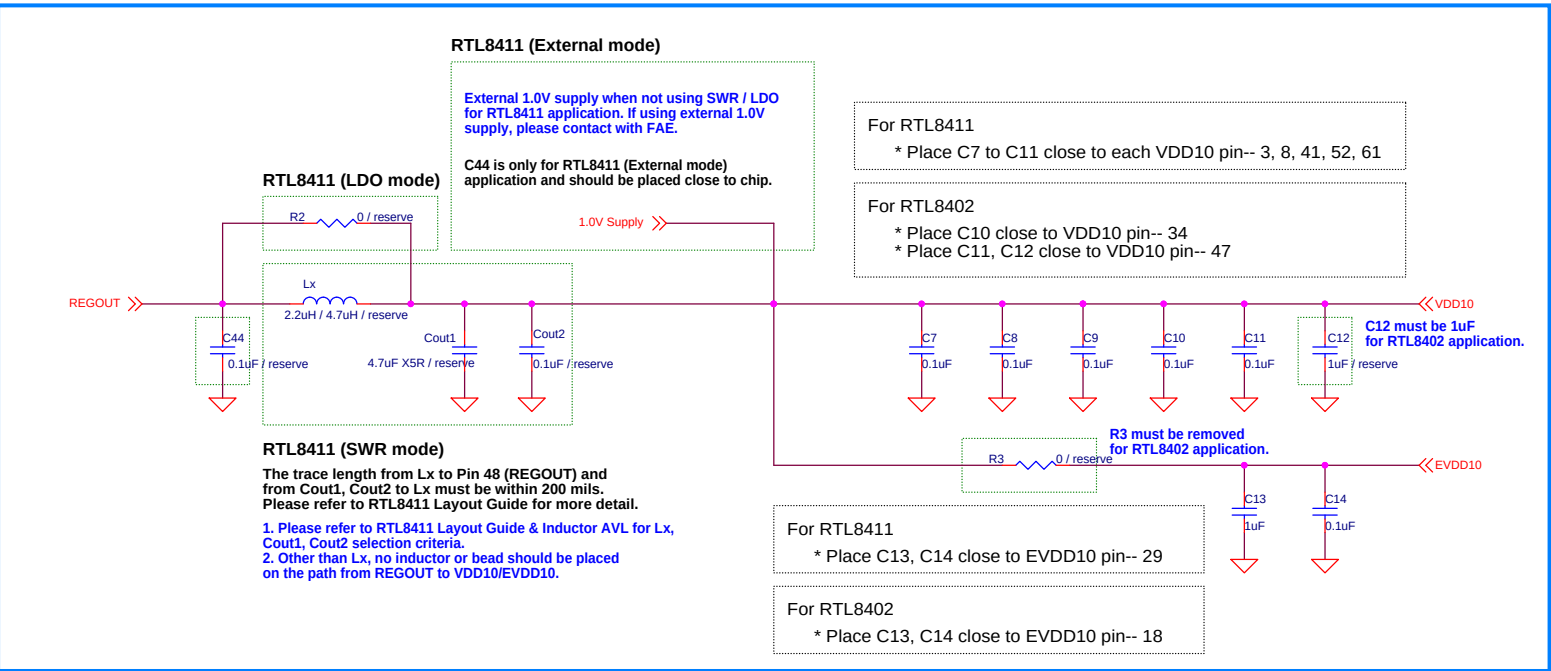
1. SWR mode only
=> R3, R30 short
2. LDO mode only
=> R2, R3, R33 short
3. External Supply only
(1) R3, R33 => short

RTL8411 Colay RTL8402

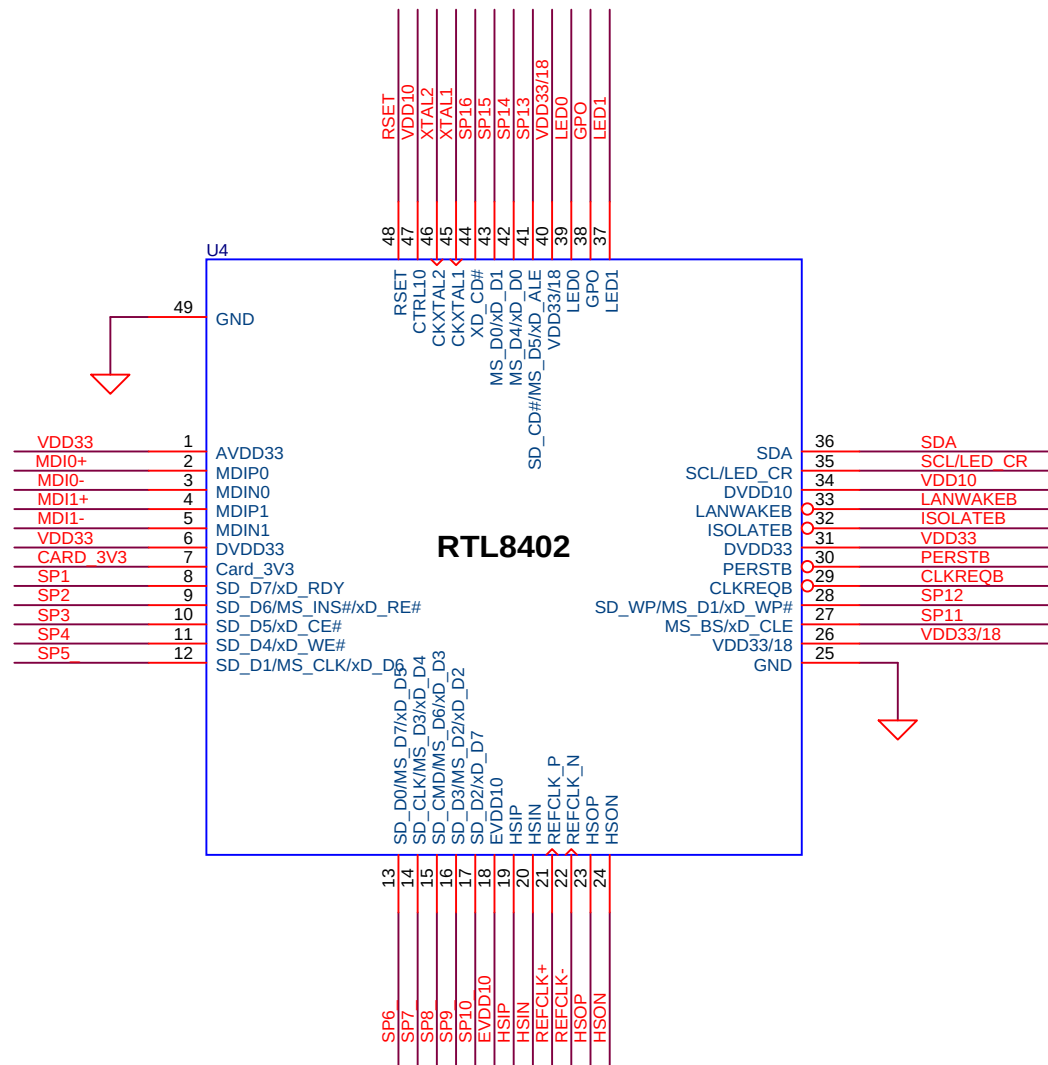
1. SWR mode only
=> R30 short
2. LDO mode only
=> R2, R33 short
3. External Supply only
(1) R33 => short

**** R3 is necessary for RTL8411 colay RTL8402****

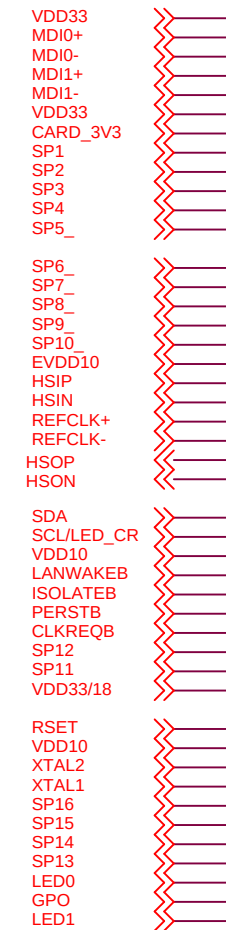
1.0V Power



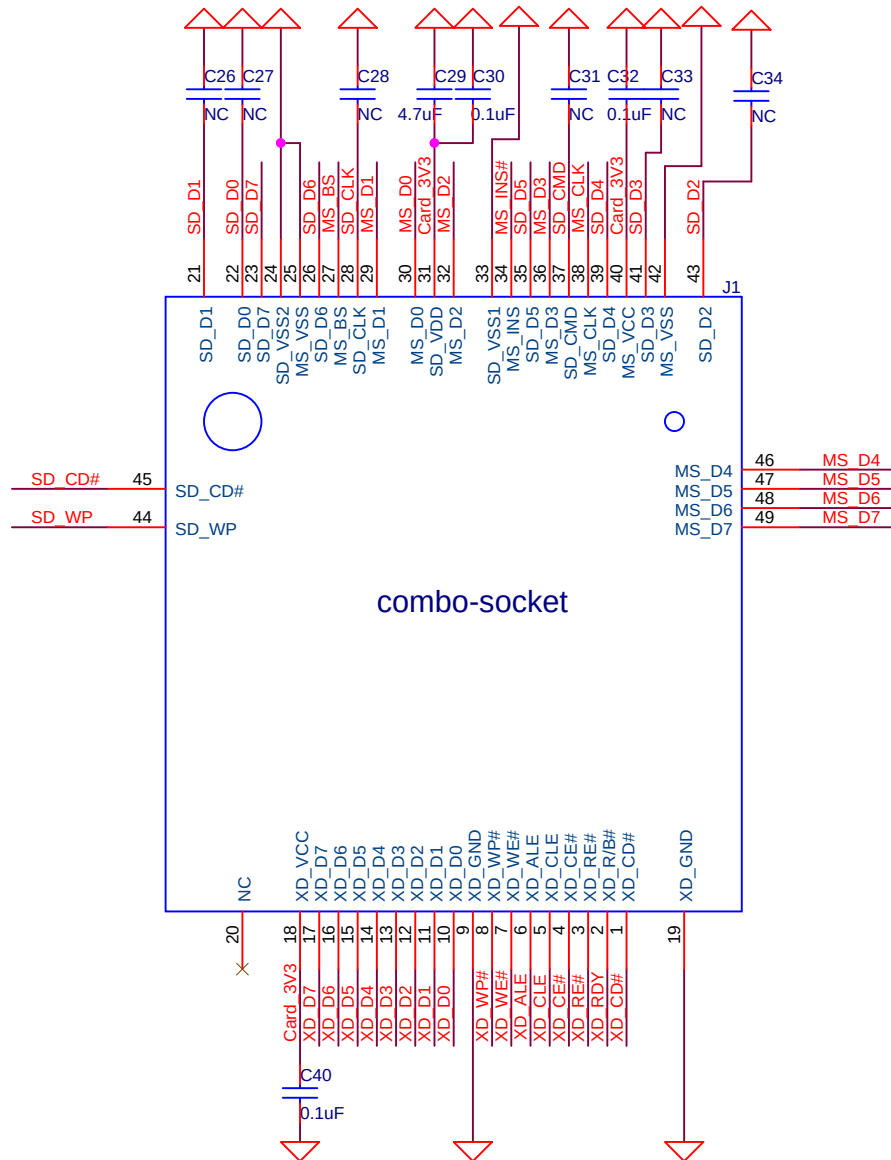
RTL8402



RTL8411 / 8402 Colay



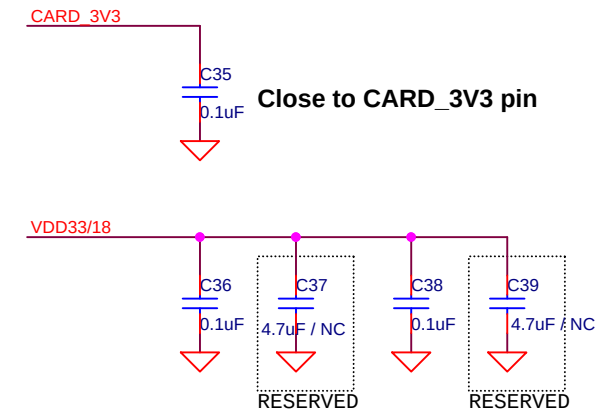
Card Reader



Share Pin

SP1	SD D7	xD RDY
SP2	SD D6	xD RE#
SP3	SD D5	xD CE#
SP4	SD D4	xD WE#
SP5	SD D1	MS CLK
SP6	SD D0	xD D5
SP7	SD CLK	MS D3
SP8	SD CMD	MS D6
SP9	SD D3	MS D2
SP10	SD D2	xD D7
SP11		MS BS
SP12	SD WP	MS D1
SP13	SD CD#	MS D5
SP14		MS D4
SP15		MS D0
SP16		xD CD#

Power



For RTL8411

- * Place C36, C37 close to VDD33/18 pin-- 33
- * Place C38, C39 close to VDD33/18 pin-- 53

For RTL8402

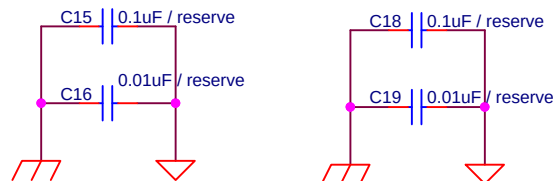
- * Place C36, C37 close to VDD33/18 pin-- 26
- * Place C38, C39 close to VDD33/18 pin-- 40

Reserved for LAN / PHY Disable Function

GPO

Connect to the control circuit, like SuperIO, with pull-high or pull-low capability. Please refer to LAN / PHY Disable Application Note.

Reserved for Noise Isolation



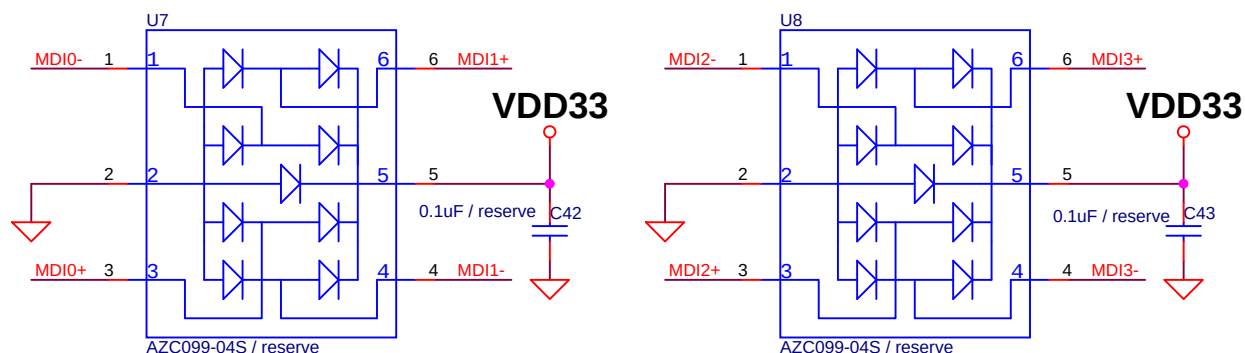
C15, C16, C18, C19 Reserved for Noise Isolation between Chassis GND and System GND (optional)

Reserved for External Clock Source

External Clock >>> XTAL2

XTAL2 is the external clock input pin. XTAL1 must be floating when using oscillator or external clock.

Reserved for TVS Component

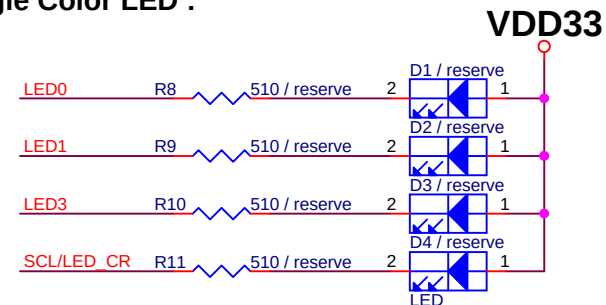


Note :

1. U7, U8 and C42, C43 reserved for line-to-line Surge and Cable ESD on the secondary side. (optional)
2. Gas Tube is for line-to-ground Surge on the primary side. (optional)

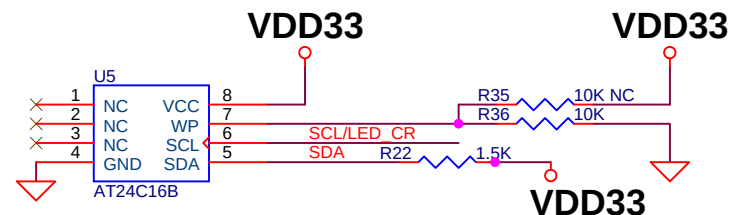
Optional LED Configuration

Single Color LED :



R8, R9, R10, R11 and D1, D2, D3, D4 Reserved for only Single Color LED Configuration (optional)

TWSI (Optional)



Change Notice from 1.00 to 1.01

- 1. Move all optional components to Page 6 (Optional Function)
- 2. Add Power selection guide to Page 2 (Power)
- 3. Add Test Point to page 3 (QFN64 Controller)
- 4. Remove Power On Latch on page 3 (QFN64 Controller)

Change Notice from 1.01 to 1.02

- 1. Revise for pin name in Page3

Change Notice from 1.02 to 1.03

- 1. Add notes for switching regulator circuits. (Page 2)
- 2. Add notes for better EMI suppression about RTL8402. (Page 3)

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Title		
RTL8411/8402 Reference Schematic : Change Notes		
Size	Document Number	Rev
A4	RTL8411/8402 Reference Schematic	103
Date:	Monday, July 15, 2013	Sheet 7 of 7